H04L 12/56 A1 WO 99/05826

(43) International Publication Date: 4 February 1999 (04.02.99)

(21) International Application Number: PCT/IB98/01117

(22) International Filing Date: 21 July 1998 (21.07.98)

(30) Priority Data:
08/900,757 25 July 1997 (25.07.97) US

(71) Applicant (*for all designated States except US*): NEXABIT NETWORKS, LLC [US/US]; Suite 390, 1700 W. Park Drive, Westboro, MA 01581 (US).

(72) Inventors; and
(75) Inventors/Applicants (*for US only*): WRIGHT, Tim [US/US]; 77 Oaks Road, Framingham, MA 01701 (US). MARCONI, Peter [US/US]; 5 Oak Tree Lane, Franklin, MA 01701 (US). CONLIN, Richard [US/US]; 32 Elm Street, Franklin, MA 02038 (US). OPALKA, Zbigniew [US/US]; 25 Quarry Lane, Harvard, MA 01451 (US).

(74) Agent: RINES, Robert, Harvey; MacLeod Allsop, Bledington Grounds, Bledington, Gloucestershire OX7 6XL (GB).

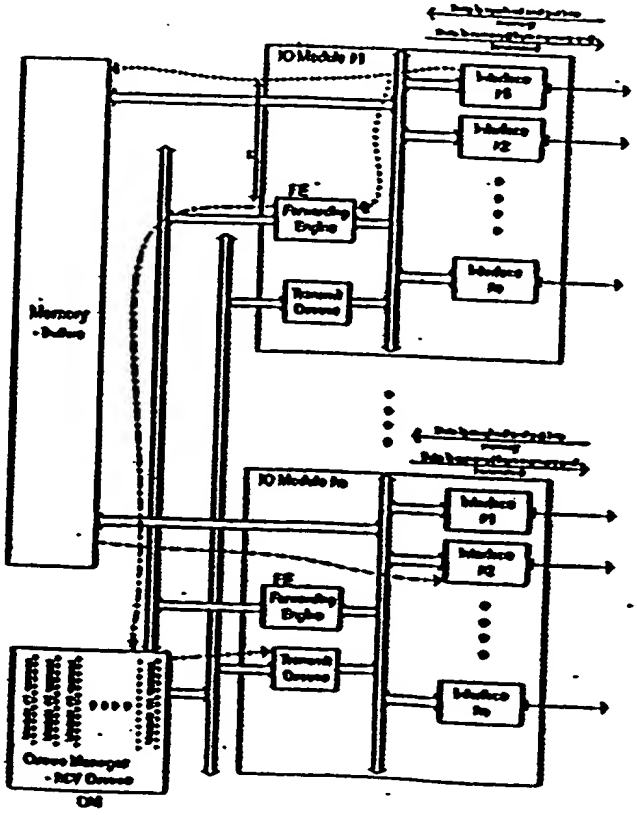
(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, HR, HU, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published
With international search report.
Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

Title: NETWORKING SYSTEMS

Abstract

A novel networking architecture and technique for cing system latency caused, at least in part, by ss contention for usage of common bus and memory ities, wherein a separate data processing and queue agement forwarding engine and queue manager are ided for each I/O module to process packet/cell ol information and delivers queuing along a separate that eliminates contention with other resources and arate from the transfer of packet/cell data into and the memory.



BEST AVAILABLE COPY